Name: Ms. Pranshu Sharma

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Mob: +91-9418661825

a. Education

Qualification	Discipline	Board/University	Year of Passing
Ph. D.	Signal Processing	IIIT Una	Pursuing
M. Tech.	Electronics & Communication Engineering	Kurukshetra University	2013
B. Tech.	Electronics and Communication Engineering	Himachal Pradesh University	2010
Diploma	Electronics and Communication Engineering	Himachal Pradesh Takniki Shiksha Board	2007
10th	General	ICSE	2004

b. Teaching Experience

Position	Institution	From	To	Nature of Work
Faculty	Indian Institute of Information Technology, Una	Jun 2019	Present	Teaching and Research
Assistant Professor	AP Goyal Shimla University, Shimla	June 2013	June 2019	Teaching and Research

c. Research Publications: -
Journals:
- **Pranshu Sharma**, Dinesh Arora, "**Designing Superdirective Patch Antenna Array Using Metamaterial**," in International Journal of Engineering Research & Technology, Vol. 1 Issue 8, October, ISSN: 2278-0181, 2012.
- **Pranshu Sharma**, Anjali Sharma, Richa Singh, "Design and Analysis of Area and Power Efficient 1-Bit Full Subtractor using 120nm Technology," International Journal of Computer Trend and Technology, Vol.88, No. 12, pp. 36-42, 2014.

- Anjali Sharma, Pranshu Sharma, "Design and Analysis of Power Efficient PTL Half Subtractor Using 120nm Technology," International Journal of Computer Trends and Technology, Vol.7, No. 4, pp. 207-213, 2014.
- **Pranshu Sharma**, Anjali Sharma, "Analysis of Power Reduction in 2 to 4 Line Decoder Design Using Gate Diffusion Input Technique, "AGU International Journal of Engineering and Technology, Vol. No. 3, pp. 25-36, 2015.

Conferences:
- **Pranshu Sharma**, Anjali Sharma**,** "POWER efficient 4-bit Subtractor design by using 1-Bit Full Adder Module," International Conference on Advances Computing and Communication Technologies, Asia Pacific Institute of Information Technology, Panipat, Haryana.
- **Pranshu Sharma**, Anjali Sharma, "VLSI Design of Power Efficient Binary to Gray Code Converter Using Gate Diffusion Input Technique," International Conference on Advances Computing and Communication Technologies, Asia Pacific Institute of Information Technology, Panipat, Haryana, Vol. No.1, pp. 185-190.
- Anjali Sharma, **Pranshu Sharma**, "Area and Power Efficient 4-Bit Comparator Design by Using 1-Bit Full Adder Module," IEEE International Conference on Parallel, Distributed and Grid Computing (PDGC 2014), pp. 1-6, 2014.

d. Short-Term Courses/FDPs Attended
- AICTE recognized National Level Short Term Course on "Embedded System and its Applications" conducted by ECE Department NITTTR, Chandigarh.
- A one-week National Level Short Term Course on "VLSI System Design" Conducted by the Electronics and Communication Department through National Institute of Technical Teachers Training and Research at AP Goyal Shimla University.
- AICTE recognized National Level Short Term Course on "Tools For Engineering Research" conducted by Electrical Engineering Department NITTTR, Chandigarh.
- AICTE recognized ICT Short Term Course on "MATLAB and LABVIEW hardware interfacing" conducted by EE Department NITTTR, Chandigarh.
- 5-Days e-STC on "Applications of Multiple Analytical Techniques in Research", organized by the Department of Management Studies, NIT Hamirpur, from 15th January to 19th January 2024.
- A one-week International Faculty Development Program on "Emerging Frontiers: Navigating the Convergence of Technology and Science for Present Progress and Future Innovation" organized by Dr. B.C Roy Engineering College, Durgapur, from 11th March to 15th March 2024.
- One-week STC on "Industrial Instrumentation and Automation (IIA 2024) organized by Siemens Center of Excellence in Collaboration with Department of Electronics & Communication Engineering, National Institute of Technology Kurukshetra, Haryana from 06th May- 10th May, 2024.

- 5-Days SERB sponsored workshop on "Artificial Intelligence Advancements in Digital Healthcare" organized by the School of Electronics, IIIT Una, from 2nd January to 6th January 2023.
- 5-Days Workshop on "Recent Developments in Machine Learning and its Applications, organized by the School of Computing, IIIT Una from 5th June to 9th June, 2023.
- A one-week SERB-sponsored short-term training program on "Recent Advancements in VLSI Design and its Applications (RAVDA-2023)" organized by the School of Electronics, IIIT Una, from 4th July to 8th July, 2023.

e. RESEARCH INTERESTS:
- Biomedical Signal Processing
- Digital Signal Processing
- VLSI

f. DETAILS: -
- GoogleScholar:https://scholar.google.co.in/citations?user=riENmqkAAAAJ&hl=en&oi=ao
- ORCiD : https://orcid.org/0009-0001-5053-4196