

CURRICULUM VITAE



Dr. Kishore Vejandla,
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Career Objective:

To secure an enterprising position in an organization where I can utilize my skills and abilities, while that offers professional growth being resourceful, innovative and flexible to the best of organization.

Educational Qualifications:

Specialization	School/college	Board/university	Year of passing	Percentage/ CGPA
PhD in RF and Optical Wireless communications and signal processing under the supervision of Dr.V.V.Mani - Professor	National Institute of Technology- Warangal (NITW)	National Institute of Technology- Warangal (NITW)	May-2021	Grade-A
M.Tech in Digital Systems and Signal Processing	Gitam Institute of Technology	GITAM University	2013	8.89 CGPA
B.Tech in Electronics and Communication Engg.	Rao and Naidu Engg. College, Ongole.	JNTU-Hyderabad	2008	74.05%
Intermediate course	Vijaya Bhatathi junior college, Ongole	Board of intermediate.	2004	90.5%
S.S.C	Z.P.High School, Ponduru.	Board of secondary education.	2002	85.16%

Recognitions:

- I was invited as a Visiting scholar during my PhD by Dr. W. O Popoola from The University of Edinburgh, Scotland, UK.

Research Collaborations:

- Engaged in collaborative research with **Dr. W. O Popoola**, University Lecturer and Chancellors Fellow, and **Dr T Ratnarajah**, professor in digital communications and signal processing, with the Institute for Digital Communications, School of Engineering at the University of Edinburgh, U.K.
- Engaged in collaborative research with **Dr. Mathini Sellathurai**, Full Professor in signal processing and intelligent systems with Heriot Watt University, Edinburgh, U.K.
- Engaged in collaborative research with **Dr. Abhinav Kumar**, Associate Professor, Indian Institute of Technology Hyderabad, India

Work Experience:

- I am working as an Assistant Professor-II (Level-10) at **IIIT Una** in the Department of School of Electronics from 29/06/2022 to till date.
- I worked as an Assistant Professor at **IIIT Dharwad** in the Department of Electronics and Communication Engineering from 07/02/2022 to 23/06/2022.
- I worked as a **Senior Research Fellow (After PhD)** in the Department of Electronics and Communication Engineering under Dr.V.V.Mani at **NIT-Warangal** from 07/02/2021 to 31/01/2022.
- I worked as an Assistant professor in **KL University, Vijayawada** in Electronics and Communication engineering department from 04/07/2014 to 27/02/2017.
- I worked as an Assistant professor in **Vignan's institute of information technology (VIIT)** (NAAC & NBA Accredited-Autonomous) Visakhapatnam from 4th Jun 2013 to till date.
- I worked as an Assistant Professor in **Malineni Lakshmaiah Women's engineering college**, Guntur (dt) in Electronics and Communication engineering department from 01/10/2008 to 01/07/2011.

Administrative skills:

- I worked as a Regional Manager for **Industrial practice school** of KL University in Hyderabad, Chennai and Vijayawada regions.

Technical Skills:

Programming Languages: Lab View programming with USRP and FPGA, Design of real time prototypes, MATLAB, Basics in C and Data structures, Verilog HDL.

Competitive exam credentials: Total 3 times I have qualified in GATE exam.

- I qualified in gate 2021 with 4245 rank
- I qualified in gate 2014 with 6299 rank.
- I qualified in gate 2008 with 2700 rank.
- I qualified in UGC-NET December-2015 for Assistant Professor

List of Journal Publications:

- [1] **K. Vejandla**, S. P. Valluri, V. M. V, S. Mathini, A. Kumar and T. Ratnarajah, "Performance Analysis Under Double Sided Clipping and Real Time Implementation of DCO-GFDM in VLC Systems," in **Journal of Lightwave Technology**, doi: 10.1109/JLT.2020.3026381. **(IEEE-SCI) (Impact factor- 4.142)**
- [2] **V. Kishore**, V. M. Vakamulla, W. O. Popoola and A. Kumar, "Implementation of Linearly Pulse Shaped Generalised Frequency Division Multiplexing for Visible Light Communication Systems," in **IEEE Open Journal of the Communications Society**, vol. 1, pp. 1614-1622, 2020, doi: 10.1109/OJCOMS.2020.3030118. **(IEEE-SCI)**
- [3] **V. Kishore**, V. S. Prasad and V. V. Mani, "A Blind Timing Synchronization Algorithm for DCO-OFDM VLC Systems," in **IEEE Photonics Technology Letters** vol. 32, no. 17, pp. 1121-1124, 1 Sept.1, 2020, doi: 10.1109/LPT.2020.3013447. **(IEEE-SCI) (Impact factor- 2.468)**,
- [4] S. P. Valluri, **K. Vejandla** and V. V. Mani, "Low complex implementation of GFDM system using USRP," in **IET Communications**, vol. 14, no. 13, pp. 2060-2067, 11 8 2020, doi: 10.1049/iet-com.2019.1106. **(IET-SCI) (Impact factor- 1.664)**
- [5] **Vejandla Kishore**, V.V. Mani, "Spectrally efficient revised PAM-DMT for IM/DD Optical Wireless Communication systems", **Optics Communications**, Volume 465, 2020, 125568, ISSN 0030-4018, <https://doi.org/10.1016/j.optcom.2020.125568>. **(Elsevier-SCI) (Impact factor- 2.508)**
- [6] S. P. Valluri, **V. Kishore** and V. M. Vakamulla, "A New Selective Mapping Scheme for Visible Light Systems," in **IEEE Access**, vol. 8, pp. 18087-18096, 2020, doi: 10.1109/ACCESS.2020.2968344. **(IEEE-SCI)) (Impact factor- 3.367)**
- [7] **K. Vejandla**, S. Valluri, V. M. Vakamulla and A. Kumar, "A Tunable Energy Signal for Intensity Modulation and Direct Detection Systems: Theory, Simulations, and Experiments," in **IEEE Photonics Journal**, vol. 12, no. 1, pp.

- 1-12, Feb. 2020, Art no. 7900412. doi: 10.1109/JPHOT.2019.2958836. (IEEE-SCI) (Impact factor-2.833)
- [8] **Vejandla Kishore**, V.V. Mani, "A DC Biased Optical Generalised Frequency Division Multiplexing for IM/DD systems", *Physical Communication*, Volume 33, 2019, Pages 115-122, ISSN 1874-4907. (Elsevier-SCI) (Impact factor-2.32)
- [9] **Vejandla Kishore**, V.V. Mani, "An LED modelled GFDM for optical wireless communications", *AEU - International Journal of Electronics and Communications*, Volume 101, 2019, Pages 54-61, ISSN 1434-8411. (Elsevier-SCI) (Impact factor-3.183)
- [10] **Vejandla Kishore**, S. Koteswararao, K.S. Srikanth, A. Jawahar, "Application of Chi-Square test and F-distribution to target maneuver detection in undersea tracking" in *International Journal of Oceans and Oceanography (IJOO)*, ISSN 0973-2667 Volume 9, Number 2 (2015), pp. 243-248. (SCOPUS)
- [11] **V. Kishore**, S.V. Rajtilak, A.Vamseekrishna, "Implementation of Blind Separation based ME in Independent Component Analysis for fast Digital Image Stabilization" in *International Journal of Engineering Research & Technology (IJERT)*, Vol. 2 Issue 11, November – 2013, ISSN: 2278-0181. (SCOPUS)
- [12] **Vejandla Kishore**, V.V. Mani, "A novel timing synchronization method for OFDM based VLC systems", *Optik*, Volume 244, 2021, 167206, ISSN 0030-4026. (Elsevier-SCI) (Impact factor-2.817)

List of Conference Publications:

- [1] **K. Vejandla**, S. Kollikonda and V. M. Vakamulla, "Performance Analysis of PAM-DMT Under Double sided Signal Clipping in IM/DD Based Systems," *2020 National Conference on Communications (NCC)*, IIT Kharagpur, India, 2020, pp. 1-6, doi: 10.1109/NCC48643.2020.9056086.
- [2] **V. Kishore**, N. Taneja and V. V. Mani, "Demonstration of energy based timing synchronization for VLC systems using SDR," *2019 2nd IEEE Middle East and North Africa COMMunications Conference (MENACOMM)*, Manama, Bahrain, 2019, pp. 1-6, doi: 10.1109/MENACOMM46666.2019.8988576.

Achievements:

- I got merit scholarship of worth Rs 25,000 from GITAM University as I got first rank in my first year
- I got a scale score 90/120 in **TOEFL** exam conducted by educational testing service (ETS).
- I got first rank in 10th class and in Intermediate in our institutes
- I got 90 marks in the exam conducted by NAC-TECH in our campus.

Strengths:

- Dedication to my work
- Positive going attitude.
- Strong determination in doing my work.
- I have ability well with a team.

Projects Undertaken:

- During M.Tech I have done a project on "Image Processing" titled as "**A Novel method for the extraction of motion vectors that causes blurriness in digital images**".
- During B.Tech (IV Year) I have done a project in "Photo diodes" titled "**Structure and Bias voltage effects on the frequency response of InGaAs/InP Photo diodes**".

Personal Profile:

Name	:	Vejandla Kishore.
Fathers name	:	V. Venkatarao.
Date of birth	:	04-05-1987.
Gender	:	Male.
Marital status	:	Single.
Nationality	:	Indian
Languages known	:	English, Telugu and Hindi.
Hobbies	:	Playing cricket, caroms.

Declaration:

I hereby declare that the information furnished above is true to the best of my knowledge and I bear the wholesome responsibility for the corrections of the particular.

Place: Una

Date: 02/08/2022.

(V.KISHORE)