# Dr. Naman Garg

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## WORK EXPERIENCE

*29 April 2022 – Present*

### Assistant Professor
Indian Institute of Information Technology, Una
- School of Electronics

*01 September 2018 – 30 September 2021*

### Assistant Professor
Institute of Engineering & Technology, Dr. Bhimrao Ambedkar University, Agra
Under TEQIP-III, NPIU (Ministry of Education)
- Dept. of Electronics & Communication Engineering

*04 January 2018 – 31 August 2018*

### Assistant Professor
Govt. Women Engineering College, Ajmer
Under TEQIP-III, NPIU (Ministry of Education)
- Dept. of Electronics & Communication Engineering

*15 October 2014 – 03 January 2018*

### Teaching cum Research Fellow
Netaji Subhas Institute of Technology, New Delhi
- Dept. of Electronics & Communication Engineering

## EDUCATION

*June 2021*

### Ph.D.
Netaji Subhas Institute of Technology, University of Delhi
(Electronics & Communication Engineering)
- Thesis Title (Some Theoretical and Numerical Studies in Quantum Filtering and Feedback Control)

*2012-14*

### M.Tech.
Netaji Subhas Institute of Technology, University of Delhi
(Signal Processing, Electronics & Communication Engineering)
- Thesis Title (Histogram equalization and Histogram specification to process the digial images)
- Aggregate GPA 8.21

*2007-11*

### B.Tech.
Sir Chhotu Ram Institute of Engineering & Technology, Meerut
(Electronics  Communication Engineering)
- Aggregate Percentage 84.04 %

## REFEREED JOURNAL PUBLICATION

1. **Naman Garg**, H Parthasarathy, DK Upadhyay, "Real-time simulation of H−P noisy Schrödinger equation and Belavkin filter", Quantum Information Processing, Springer 16 (5), 121, 2017.(SCI IF: 2.349)

2. **Naman Garg**, H Parthasarathy, DK Upadhyay," Belavkin filter for mixture of quadrature and photon counting process with some control techniques", Quantum Information Processing, Springer 17 (3), 59, 2018. (SCI IF: 2.349)

## CONFERENCE PUBLICATION

1. **Naman Garg**, G Srivastava, PS Sengar, "Segmentation based extended piecewise maximum entropy histogram for image enhancement", IEEE International Conference on Signal Processing and Integrated Networks (SPIN), pp. 168-173, 2014.

2. **Naman Garg**, A Angra, P Sengar, "Image enhancement by scaling the discrete wavelet coefficients based on segmentation", 2nd IEEE International Conference on Signal Processing and Integrated Networks (SPIN), pp. 520-525, 2015.

3. Prateek Singh Sengar, Surabhi Singh, **Naman Garg**, "A constrained re-growing concept for object separation in sea images", 6th IEEE International Conference on Cloud System and Big Data Engineering (Confluence), pp: 421-424, 2016.

4. Rajat Modi, **Naman Garg**, Harish Parthasarathy, "Steganography using wavelets with statistical performance analysis", 1st International Conference on Electronics, Materials Engineering and Nano-Technology (IEMENTech), pp: 1-8, 2017.

5. **Naman Garg**, Harish Parthasarathy, D. K. Upadhyay, "Study and Implementation of Belavkin Filter with control and entropy computation", ICPR Springer AIACTR, Delhi,2017.

6. **Naman Garg**, Harish Parthasarathy, D. K. Upadhyay, "Belavkin filter for estimating the electron spin", ICMAE, Indore, 2018.

## SUBMITTED JOURNAL PUBLICATION

1. **Naman Garg** and H Parthasarathy, "Quantum Filter for estimating the spin of an electron", Quantum Studies: Mathematics and Foundations, Springer.

2. **Naman Garg** and H Parthasarathy," Belavkin Filter with control and entropy computation", Advanced Quantum Technologies, Wiley.

## SPONSORED RESEARCH PROJECT

TITLE — Development and Application of Quantum Schrodinger Communication Channel based on Classical Sources

Amount — 15.37 Lacs

Sponsoring Agency — NPIU, Ministry of Education under CRS scheme of TEQIP-III

Role — Principal Investigator

## AWARDS

Research Award
- **Young Scientist** Award for outstanding contribution in Research in ICPR, Springer 2017 held at AIACTR Delhi.

Student Award-NSIT, New Delhi
- **Teaching-cum-Research Fellowship** during 2014-18

- **MHRD GATE PG Scholarship** during 2012– 2014

Student Award-SCRIET Meerut
- **University Gold Medal** in B.Tech.

**CONFERENCES/FDP/STTP ORGANISED AS CONVENER/COORDINATOR**

1. National Conference on "Recent Advances in Electronics & Communication Engineering" during 29-30 January 2021 under TEQIP-III, MoE as Convener.

2. One week Faculty Development Programme on "Recent Trends in Electronics & Communication Engineering" during 29 July-02 August 2019 under TEQIP-III, MoE as Convener.

3. One-week Faculty Development Programme on "Advancements in Signal Processing Optimization Techniques" jointly organized by Electronics and ICT Academies held from June 3 – 7, 2019 as Local Coordinator.

4. One Week Faculty Development Programme on "Introduction to Programming: A Pedagogical Approach" jointly organized by Electronics and ICT Academies held from 17 – 22 June 2019 as Local Coordinator.

5. One Week Faculty Development Programme on "Python Programming with Industry Perspective" jointly organized by Electronics and ICT Academies held from December 2 – 6, 2019 as Local Coordinator.

6. One-week Faculty Development Programme on "Deep Learning and Applications" jointly organized by Electronics and ICT Academies held from December 9 – 13, 2019 as Local Coordinator.

7. One-week Faculty Development Programme on "VLSI Chip Design Hands-on using Open Source EDA" jointly organized by Electronics and ICT Academies held from December 16 – 20, 2019 as Local Coordinator.

8. One-week Faculty Development Programme on "AI and Machine Learning" jointly organized by Electronics and ICT Academies held from December 23 – 27, 2019 as Local Coordinator.

9. One-week Faculty Development Programme on "Natural Language Processing" jointly organized by Electronics and ICT Academies held from January 06 – 10, 2020 as Local Coordinator

10. One-week Faculty Development Programme on "ICT Tools for Teaching, Learning Process Institutes" jointly organized by Electronics and ICT Academies held from January 13 – 17, 2020 as Local Coordinator.

**FDP/STTP/PDTs ATTENDED**

1. One week Faculty Development Program on "Mobile Computing and Communication" during 03-07 July 2017 at **NIT Delhi**.

2. Five day Faculty Induction Workshop on "Pedagogy, Research and TEQIP Execution" during 12-16 February 2018 at **IIT Bombay**.

3. TEQIP-III Sponsored Faculty Development Programme on "Quality, Reliability and Maintainability Aspects in Industry" during 02-06 July 2018 at **NIT Jalandhar**.

4. Workshop on "Dynamics of Entrepreneurship Innovation" during 23-27 July 2018 at **GWEC Ajmer**.

5. Traning on "3D Printing Robotics Technology" during 07-10 August 2018 at **Centre for Electronic Governance, Jaipur**.

6. Professional Development Training for Heads of Departments during 10-14 June 2019 at **IIM Udaipur**.

7. Short Term Course under TEQIP-III on "Advanced Pedagogies: Active Learning and Digital Tools" during 01-05 July 2019 at **IIT Delhi**.

8. "Professional Development Training" during 04-06 November 2019 at **IIM Tiruchirappalli**.

9. MHRD-TEQIP-III KITE Course on "Internet of Things" during 27-31 January 2020 at **IIT Bombay**.